

ASTRO AL Series

AMPLIFIERS FOR HOUSE DISTRIBUTION

- Aluminium die cast housing with high screening and cooling
- According to latest specifications EN 50083-2/A1
- Optimal protection against ingress
- Continue tuneable gain and slope adjustment
- Now with active and passive 65 MHz return path
- Excellent price / performance ratio



TYPE		AL 1	AL 1 R	AL 1 RE	AL 3 E	AL 3 RE
Frequency range	[MHz]	47-862	47-862	47-862	47-862	47-862
Return path	[MHz]	–	5-30, pas.	5-30, pas.	–	5-30, pas.
Gain	[dB]	20	20	20	30	30
Noise figure	[dB]	5,5	6	6	6,5	6,5
Output level	[dBμV]	113	113	113	116	116
		60 dB KMA / EN 50083-3				
Output level	[dBμV]	97	97	97	100	100
		CTB, CSO > 60 dB EN 50083-3, CENELEC, 42 Channels				
Attenuation	[dB]	0-10	0-10	0-10	0-10	0-10
Slope correction	[dB]	–	–	0-20	0-20	0-20
Supply voltage		230 V~, 50 Hz, 3 VA EN 50083-3, Category C			230 V~, 50 Hz, 7 VA EN 50083-3, Category C	
RETURN PATH						
Frequency range	[MHz]	–	5-30	5-30	–	5-30
Through loss	[MHz]	–	< 2	< 2	–	< 2
Return loss	[dB]	EN 50083-3, Category C				

TYPE		AL 223 G	AL 331 G	AL 332 G	AL 431 G
FORWARD PATH					
Frequency range	[MHz]	86-1006			
Gain	[dB]	24 ± 1	32 ± 1	30 - 34	36 ± 1
Interstage Slope (fix), typ.	[dB]	3			
Noise figure typ.	[dB]	6	6	5	5
Input equalizer	[dB]	0-15			0-18
Input attenuation	[dB]	0-20			0-20
MAX.OUTPUT LEVEL					
42 channels 60 dB CSO, CTB linear	[dBμV]	97	98	100	106
RETURN PATH					
Frequency range	[MHz]	5-65			
Gain	[dB]	22 ± 1	26 ± 1	26 ± 1	27 ± 1
Interstage Slope (fix), typ.	[dB]	3			
Noise figure typ.	[dB]	6	6	6	6
Input attenuation	[dB]	0-15			
MAX. OUTPUT LEVEL					
2 transmitter, linear 60 dB IMA2 (EN 50083-3)	[dBμV]	105			
3 transmitter, @ 60 dB KMA (DIN 45004B)	[dBuV]	110			

COMMON DATA					
Wave resistance	[Ω]	75			
Return loss In-/output		EN 50083-3, Cat. C			
Temperature range	[°C]	- 15 ... + 55			
Connectors		F Jacks 75 Ω			
EMV		acc. EN 50083 -2			
Supply voltage	[V~/Hz]	230 / 50			
Power consumption	[W]	7,5 / 4,5	7 / 5	8 / 6	9,5 / 7,5
Dimensions	[mm]	117 x 132 x 50			
Weight	[kg]	0,8			
Protection		DIN 45 050-IP 20			

BLOCK DIAGRAM

